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# PROFILE

Dynamic and results-driven professional with a strong background in Electronics and Telecommunication Engineering. Experienced in designing, developing, and improving electronic circuits, communication systems, and embedded technologies. Skilled in working with cross-functional teams to solve technical challenges, enhance system efficiency, and ensure smooth hardware-software integration. Proficient in analyzing system requirements, troubleshooting issues, and implementing innovative solutions. Strong problem-solving abilities, project management skills, and a keen interest in emerging technologies to improve communication and electronic systems.

# EDUCATION

## Veermata Jijabai Technological Institute (VJTI), Mumbai Pursuing: Aug 2023 – June 2025

*Master of Technology: Defence Technology* (Aerospace Technology Specialization)CGPA: *7.93*

## Dr. Babasaheb Ambedkar Technological University Lonere, Raigad Passed: Aug 2020- June 2023

*Bachelor of Technology: Electronics and Telecommunication Engineering* CGPA: 8.6

**Government Polytechnic Pune** Passed: Aug 2016- June 2020

Diploma in Engineering: Electronics and Telecommunication Percentage: 74.60

# TECHNICAL SKILLS AND INTEREST

* Embedded Systems Development
* PCB Design and Circuit Analysis
* Microcontrollers (STM32, Raspberry Pi, Arduino)
* Digital and Analog Communication Systems
* Signal Processing and Wireless Communication
* IoT and Sensor Integration
* MATLAB, Simulink, and LabView
* FPGA and VHDL/Verilog
* Protocols: UART, SPI, I2C, CAN, TCP/IP
* RF and Antenna Design
* Troubleshooting and Debugging Electronic Systems

# EXPERIENCE

**- Bharat Forge Limited** July 2024 – June 2025

M. Tech Project Intern - Research & Development Pune

***Project Name*** *–* Design and Development of Power Distribution Board for Autonomous Underwater Vehicle.

- Designed and developed a power distribution board to efficiently manage and regulate power for the AUV.

- Created a custom PCB layout to ensure stable power distribution across all onboard components.

**-** Integrated multiple power sources with voltage regulators and battery management systems for optimal performance.

- Implemented overcurrent, overvoltage, and short-circuit protection to enhance system reliability.

- Programmed microcontrollers for real-time power monitoring, fault detection, and system diagnostics.

- Conducted simulations using ALTIUM & PROTEUS and performed hardware testing to validate power efficiency and stability.

**- Maxgen Technologies Pvt. Ltd** Jan 2023 – July 2023

Trainee Design Engineer Pune

***Project Name*** *–* AWS Architect

- Designed and implemented scalable, secure, and cost-efficient cloud architectures on AWS.

- Optimized cloud infrastructure for performance, reliability, and cost-effectiveness using AWS Well Architected Framework.

- Migrated on-premise applications to AWS, ensuring minimal downtime and high availability.

**- FlyLab Solution Pvt. Ltd** July 2024 – June 2025

Research and Development Intern Nashik

***Project Name*** *–* Design, Development and Optimization of Advanced Electronics Systems for Unmanned Aerial Vehicles (UAVs*)*

-Successfully designed, built, and customized unmanned aerial vehicles (UAVs) or drones for various applications.

- Conducted mapping missions using UAVs, resulting in accurate data collection for analysis and decision- making.

**-**Integrated multiple power sources with voltage regulators and battery management systems for optimal performance.

- Assisted in testing and troubleshooting UAV systems to ensure functionality and performance.

# PERSONAL PROJECT

**Bluetooth Enabled Voice Controlled Multipurpose Car**

* Designed and developed a voice-controlled robotic car using Bluetooth communication.
* Integrated a microcontroller (Arduino/Raspberry Pi) to process voice commands and control motor movements.
* Implemented a mobile application for sending voice commands via Bluetooth module.
* Optimized power management to ensure efficient battery usage and longer operational time.

**Implementation of Full Adder using NAND Gates**

* Designed and simulated a full adder circuit using only NAND gates in Microwind software.
* Developed the transistor-level layout for the full adder and optimized it for area and power efficiency.
* Verified the logical functionality using Digital Schematic Editor before layout implementation.

## HOBBIES

- Running  
- Reading & Researching

- Travelling

- Listing Music

- Singing